

[02207/12116]

1838 A IFM

## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s)

Raj NAIR et al.

Serial No.

10/016,793

Filed

October 26, 2001

For

SILICON INTERPOSER-BASED HYBRID VOLTAGE

REGULATOR SYSTEM FOR VLSI DEVICES

Art Unit

2838

Examiner

Rajnikant B. Patel

Mail Stop Amendment Commissioner for Patents P.O. Box 1450

Alexandria, VA 22313-1450

I hereby certify that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail in an envelope addressed to: Mail Stop Amendment, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450 on

Date:

July 7, 2004

Signature:

Howard I. Grossman, Reg. No. 48,673

## **AMENDMENT TRANSMITTAL**

SIR:

Please find an Amendment transmitted herewith for filing in the above-identified patent application. Applicants respectfully request a one-month extension of time in which to respond to the office action dated March 17, 2004, for which a response period expiring on June 17, 2004 was set. The extended period expires on July 17, 2004.

Please charge the \$110.00 extension fee and any additional fees required to Deposit Account No. 11-0600. A duplicate copy of this Transmittal is enclosed.

Respectfully submitted, KENYON & KENYON

Dated:

July 7, 2004

Howard I. Grossman

(Reg. No. 48,673)

07/13/2004 SMINASS1 00000058 110600 10016793

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[2207/12116]

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## **AMENDMENT**

SIR:

In response to the Office Action mailed on March 17, 2004, please reconsider the above-identified application based on the following.

Amendments to the Claims are reflected in the listing of the claims which begins on page 2 of this paper.

Remarks begin on page 6 of this paper.